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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Iwen Chao et al.

Title: SEMICONDUCTING DEVICE WITH FOLDED INTERPOSER

Docket No.: 884.C26US1

Filed: March 29, 2004

Examiner: Monica Lewis

Serial No.: 10/812,277

Due Date: September 8, 2005

Group Art Unit: 2822



Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

- Return postcard.  
 Response to Restriction Requirement (3 pgs.).

If not provided for in a separate paper filed herewith, Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Customer Number 21186

By: Ann M. McCrackin

Atty: Ann M. McCrackin

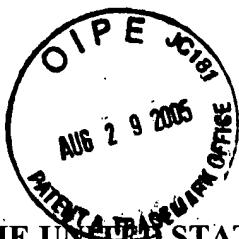
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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 25 day of August, 2005.

John D. Bryant-Watson  
Name

John D. Bryant-Watson  
Signature

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
(GENERAL)



S/N 10/812,277

PATENT

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Assignee:	Intel Corporation	Customer Number:	21186

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RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In response to the Restriction Requirement mailed August 9, 2005, Applicants hereby elect, with traverse, Group I (claims 1-7 and 18-20). Applicants further hereby elect, with traverse, claims 1-7 of Group I. **If the restriction is continued, Applicants will cancel remaining claims 8-20 (with prejudice), and will reserve the right to reintroduce them in one or more divisional applications at a later date.**

The Restriction Requirement is traversed on the basis that Restriction Requirements are optional in all cases. MPEP 803. If the search and examination of an entire application can be made without serious burden, the Examiner must examine it on the merits, even though it arguably may include claims to distinct or independent inventions. MPEP 803.

All of the pending claims relate to semiconducting devices, electronic systems and methods that include (i) an interposer having a fold which divides the interposer into a first section and a second section; (ii) a first die and a contact that are each attached to the first surface of the interposer at the first section and the second section; and (iii) a second die attached to a second surface of the interposer where the second die is stacked onto the first die and electrically coupled to the first die by the contact and the interposer. The Examiner will be looking for an interposer, a contact and first and second dies as recited in each of claims 1-20. Thus, Applicant respectfully submits that these claims can all be easily searched and examined together.